

# Eddie Rodriguez

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## Professional Experience

**Real-Time Software Architect – Keysight Technologies**, April 2018 – Present

*Aerospace Defense Government Solutions group*

- Responsible for qualifying and defining DSP and software capabilities for customer real-time systems
- Work with RADAR & EW product lines

**Principal RF Systems Engineer - National Instruments Corporation**, Summer 2012 – April 2018

*Americas Aerospace & Defense Segment*

- Started as Staff RF Systems Engineer, promoted to Senior and then Principal Systems Engineer
- Work closely with largest segment customers to scope and create proof of concept systems to demonstrate SDR and HPC platform capabilities and benchmarks.
- Work with Marketing and R&D to identify and prioritize product roadmaps and features based on customer and industry requirements. Presented to CEO and Sr. VP leadership
- Consistent positive feedback from RF Business Development and Salesforce teams for help with customer engagements. Won Outstanding Sales Enablement Award at 2015 Americas Sales Conference
- Mentor and tech lead for other Systems and Application Engineers
- Deliver internal and customer presentations; perennial presenter at annual industry conference:
- Digital Signal Processing at Gigasamples/Second - NI Week 2018
- Direct RF – All Digital SDR - NI Week 2016
- Multichannel RF - Technical Overview – Global SE Training Europe 2015
- T&M Receiver Architectures for use in SIGINT, EW and Radar Applications – AOC webcast, March 2015
- et. al.

**Staff Digital Hardware Engineer - National Instruments Corporation**, Summer 2006 - Summer 2012

*Compact DAQ & Conditioned Measurements R&D Groups*

- Started as hardware intern, promoted to Digital Hardware Engineer and then Staff Hardware Engineer
- Development HW engineer including: VHDL logic design & verification, PCB design & layout, leading products through the hardware release process, supporting the software driver team, and helping define the feature sets
- Lead engineer for the [cDAQ 9191/9181](#) backplane. Designed hardware backplane for the product
- Worked with software team to help bring up embedded Linux operating system and tweak C based WIFI driver.
- Development VHDL engineer for the [NI 9683](#).
- Worked with Architectures & Technologies team on the development of a digital ASIC.
- Taught Digital Design Best Practices course (internal course for all digital HW engineers)
- Associate Tech Lead for HDL and Board Design.
- Supervised interns and new hardware engineers

**Digital Hardware Intern - International Business Machines Corporation**, Summer 2004

*Thomas J. Watson Research Center – Advanced Display Technology Laboratory*

- Developed a VHDL Frame Grabber for a Camera Link interface on a Xilinx Virtex-II Pro FPGA

## Education

**Rice University**, May 2006, Bachelor of Science in Electrical Engineering  
Magna Cum Laude, GPA 3.98/ 4.0 scale

## Certifications

DoD Security Clearance  
Certified LabVIEW Architect, Certified TestStand Architect